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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10030089	FILING DATE 01/14/2002	CLASS 257	SUBCLASS 368	GAU 2811	EXAMINER 2814 P. CAO
<b>**APPLICANTS:</b> Sakamoto Kazuhisa;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A 371 OF PCT/JP00/04715 07/13/2000 yes, pc					
<b>BEST AVAILABLE COPY</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 11/201875 07/15/1999 yes, pc					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 107400-00045	
Verified and Acknowledged Examiners's initials pc					
TITLE : Semiconductor device including mos field-effect transistor					
<small>U.S. DEPT. OF COMM./PAT. &amp; TM.-PTO-436L (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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